

IEC QUALITY ASSESSMENT SYSTEM (IECQ)

Schedule of Scope to Certificate of Approval

Independent Testing Laboratory

IECQ Certificate No.: IECQ-L CEP 24.0001-01 CB Certificate No.: IECQ-L 2024.006

Schedule Number: IECQ-L CEP 24.0001-01-S Rev No.: 3 Revision Date: 2024/10/24 Page 1 of 3

No	Products, Materials	I	tems,Parameter	Title, Code of specification, standard or method used
IN2		N⁰	Items,Parameter	
	Electrical and electronic products/electronic semiconductor products	1	Steady-State Temperature-Humidity test	《Environmental testing of electrical and electronic products - Part 2: Test methods - Test Cab: Constant damp heat test》 GB/T 2423.3- 2016 《Environmental testing of electrical and electronic products - Part 2: Test methods - Test Cy: Constant wet heat is mainly used for accelerated testing of components》 GB/T 2423.50-2012
		2	Temperature cycle test	《Environmental tests - Part 2: Test methods - Test N: temperature change》 GB/T 2423.22-2012
		3	Temperature storage test	《Environmental testing for electrical and electronic products - Part 2: Test methods - Test A: Low temperature》 GB/T 2423.1-2008
				《Environmental testing for electrical and electronic products - Part 2: Test methods - Test B: High temperature》 GB/T 2423.2-2008
1		4	Visual inspection of appearance	Test methods and procedures for microelectronic devices GJB-548C-2021 Method 2009.2
		5	Decap	Methods for destructive physical analysis of military electronic components GJB-4027B-2021 in 1103 in 2.6.2
		6	Delayer	Test Methods and Procedures for Microelectronic Devices GJB-548C-2021 Method 5003 in 3.3.3
		7	IV CURVE	Test methods and procedures for microelectronic devices GJB-548C-2021 Method 5003 in 3.2.3.3.2.4
		8	InGaAs/OBRICH	Test methods and procedures for microelectronic devices GJB-548C-2021 Method 5003 in 3.4(d)
		9	PROBE	Test Methods and Procedures for Microelectronic Devices GJB-548C-2021 Method 5003 in 3.2.12
		10	SAT	Physical Analysis Methods for Destructive Analysis of Military Electronic Components: GJB- 4027B-2021, Method 1103, 2.5

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China Electronic Product Reliability and Environmental Testing Research Institute (CEPREI)

No. 76 West of Zhucun Avenue, Zhucun, Zengcheng District, Guangzhou 511370 China

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Schedule	Nur	mber: IECQ-L CEP 24.00	01-01	-S Rev No.: 3	Revision Date: 2024/10/24 Page 2 of 3
		Nonhermetic Surface Mount Devices /Electronic Semiconductor Products	1	Pre-condition test	《Preconditioning of Nonhermetic Surface Mount Devices Prior to Reliability Testing》 JSED22-A113I-2020 Clause5 《Moisture/Reflow Sensitivity Classification for Non-hermetic Surface Mount Devices (SMDs) 》 J-STD-020F-2022 Clause5
					《Failure Mechanism Based Stress Test Qualification For Integrated Circuits》 AEC- Q100-REV-J 《Failure Mechanism Based Stress Test Qualification For Discrete Semiconductors》 AEC-0101-RCV-E
			2	Temperature and humidity bias test	
	2		3	High temperature bias test	Q100-REV-J 《Temperature, Bias, and Operating Life》 JESD22-A108G-2022 Clause4
					《Failure Mechanism Based Stress Test Qualification For Integrated Circuits》 AEC- Q100-REV-J
			4	High temperature storage test	《High Temperature Storage Life 》 JESD22-A103E.01-2021 Clause4
					《Failure Mechanism Based Stress Test Qualification For Integrated Circuits》 AEC- Q100-REV-J
			5	High acceleration stress test	《Accelerated Moisture Resistance - Unbiased HAST》 JESD22-A118B.01-2021 Clause4
					《Highly Accelerated Temperature and Humidity Stress Test (HAST)》 JESD22-A110E.01-2021 Clause4
					《Failure Mechanism Based Stress Test Qualification For Integrated Circuits》 AEC- Q100-REV-J
					《Failure Mechanism Based Stress Test Qualification For Discrete Semiconductors》 AEC-Q101-RCV-E

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	Autoclave test	《Accelerated Moisture Resistance - Unbiased Autoclave 》 JESD22-A102E-2015 Clause4
6		《Failure Mechanism Based Stress Test Qualification For Integrated Circuits》 AEC- Q100-REV-J
		《Failure Mechanism Based Stress Test Qualification For Discrete Semiconductors》 AEC-Q101-RCV-E
	Temperature cycle test	《Temperature Cycling》 JESD22-A104F.01- 2023 Clause4,B,C
7		《Failure Mechanism Based Stress Test Qualification For Integrated Circuits》 AEC- Q100-REV-J
		《Failure Mechanism Based Stress Test Qualification For Discrete Semiconductors》 AEC-Q101-RCV-E

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