

## IEC QUALITY ASSESSMENT SYSTEM (IECQ)

covering Electronic Components,

Assemblies, Related Materials and Processes

For rules and details of the IECQ visit www.iecq.org

## Schedule of Scope to Certificate of Approval Approved Process - Capability Approval

IECQ Certificate No.: IECQ-C BSI 14.0008

CB Certificate No.: E093/CA

Schedule Number: IECQ-C BSI 14.0008-S Rev No.: 3 Revision Date: 2015/05/01 Page 1 of 1			
Board Types:	Multilayer Double sided with plated-through holes Single and double sided with plain holes	BS CECC 23300-003 BS CECC 23200-003 BS CECC 23100-003	
Base Materials:	Epoxide Woven Glass Ceramic filled thermoset/woven glass		
Board Size:	520 mm x 370 mm Maximum	BS EN 123 200, BS EN 123 100	Boroughbridge
	520 mm x 300 mm Maximum	BS EN 123 300	Boroughbridge
	580 mm x 480 mm Maximum	BS EN 123 200 BS EN 123 100	Knaresborough
Conductors:	Minimum Width: Minimum Spacing:	100μm <u>+</u> 20μm 100μm <u>+</u> 20μm	
Number of Layers	14 Maximum 2 Maximum		Boroughbridge Knaresborough
Plated-through Hole Diameter:	0.25 mm drilled hole (0.15 mm finished hole)		
Aspect Ratio: Finishes:	9.6 : 1 Maximum Hot Air Solder Leveled SnPb <sup>(1)</sup> Lead Free (Pb-Free) Hot Air Solder Levelled Tin <sup>(1)</sup> Immersion Silver <sup>(2)</sup> Electroless Nickel/Immersion Gold <sup>(2)</sup>		
Additional:	Marking Inks: Screen Printed UV Cure Liquid Photoimageable Ident ink (Thermal Cure) Ink Jet applied UV Cure Gold Edge contacts : 2.5 microns Gold on 3.0 - 5.0 microns Nickel over Copper <sup>(3)</sup>		
It may not be possible to achieve all the limits of the capability in combination. Such combinations are determined by the agreed customer detail specification for the printed circuit board ordered.			etermined by the

<sup>(1)</sup> Meets the requirement for Solderability after accelerated ageing using Activated Flux

- <sup>(2)</sup> Meets the requirements for Solderability using Activated Flux
- <sup>(3)</sup> Sub-contracted Process for Edge contacts of assessed porosity

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