



IEC QUALITY ASSESSMENT SYSTEM (IECQ)

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Schedule of Scope to Certificate of Approval

Independent Testing Laboratory

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№	Products, Materials	Items,Parameter		Title, Code of specification, standard or method used
		№	Items,Parameter	
1	ESD & LATCH-UP CAPABILITY TEST FOR INTEGRATE CIRCUIT AND COMPONENT	1	Electrostatic Discharge Sensitivity Testing Human Body Model	ANSI/ESDA/JEDEC JS-001 (2024) Mil Std 883-K (2017) Method 3015.9 AEC Q100-002-E(2013) AEC-Q101-001-REV-A(2005)
		2	Electrostatic Discharge Sensitivity Testing Machine Model	JESD22-A115-C(2010) ESDA STM 5.2(2012)
		3	Field-Induced Charge-Device Model Test Method for Electrostatic-Discharge- Withstand Thresholds of Microelectronic Components	ANSI/ESDA/JEDEC JS-002 (2022) AEC Q100-011-REV-D(2019) AEC-Q101-005-REV-A(2019)
		4	IC Latch-up Test	JESD78F.02(2023)

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